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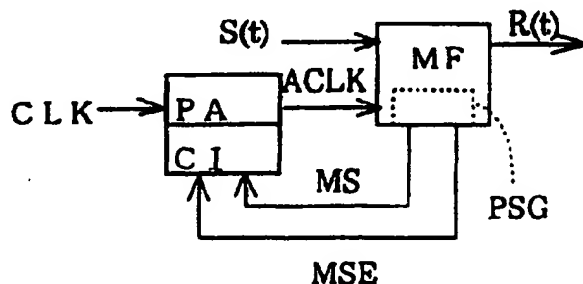
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(54) **Matched filter circuit**

(57) The present invention has an object to provide a matched filter circuit which is possible to synchronize a spreading code with an input signal. A matched filter according to the present invention samples input signal

in response to three clocks from the first to the third shifted by a half cycle of a sampling signal so as to judge whether the sampling clock is ahead or behind of the input signal according to signs of input signal sampled. One clock is selected to be the sampling clock.

Fig. 1





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EUROPEAN SEARCH REPORT

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| Place of search MUNICH | | Date of completion of the search 16 October 2001 | Examiner Giglietto, M |
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